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**APPLICANT**

N. KANEKAWA, et al

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**GROUP**

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## U.S. PATENT DOCUMENTS

[illegible]

## FOREIGN PATENT DOCUMENTS

[illegible]

**OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)**

W	Data Book "Hitachi IC Memory Data Book", HITACHI, HB56A49 Series, 4194304 – word x 9-bit High Density Dynamic RAM Module, pp. 1-8 (English Translation), 1979.
W	Data Book "Hitachi IC Memory Data Book 2", HITACHI, HB56D51236 Series 524288-word x 36-bit High Density Dynamic RAM Module, pp. 1-10 (English Translation), 1988.

**EXAMINER**

V. N. Kravchenko

**DATE CONSIDERED**

10/29/05

**EXAMINER:** Initial if citation is considered, draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

(Form PTO-1449 [6-4])

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